

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: : Group Art Unit:  
Aguilar, et. al. : Intellectual Property Law Department  
Serial No.: : International Business  
Filed: : Machines Corporation  
Title: System and Method for Task Queue : 11400 Burnet Road  
Management of Virtual Devices : Austin, Texas 78758  
Using a Plurality of Processors :

INFORMATION DISCLOSURE STATEMENT

The Assistant Commissioner of Patents  
Washington, D.C. 20231

Sir:

Applicants submit herewith patents, publications or other information of which they are aware, which they believe may be material to the examination of this application and in respect of which there may be a duty to disclose in accordance with 37 CFR § 1.56.

While this Information Disclosure Statement may be "material" pursuant to 37 CFR § 1.56, it is not intended to constitute an admission that any patent, publication or other information referred to herein is "prior art" for this invention unless specifically designated as such.

In accordance with 37 CFR § 1.97(g) the filing of this Information Disclosure Statement shall not be construed to mean that a search has been made or that no other material information as defined in 37 CFR § 1.56 (a) exists. The attached form, PTO-1449, provides a listing of patents, publications, or other information as required by 37 CFR § 1.98 (a)(1). A copy of each of the non-U.S. Patent documents on PTO-1449 is supplied herewith.

This application is being filed on the same day as the following U.S. Patent Applications, each having the same assignee (International Business Machines Corporation):

"System and Method for Loading Software on a Plurality of Processors," by Aguilar, Chow, Day, Gowen, Nutter, and Xenidis (Docket No. AUS920030717US1);

"System and Method for Processor Thread Acting as a System Service Processor," by Aguilar, Nutter, and Stafford (Docket No. AUS920030700US1);

"System and Method for Processor Thread for Software Debugging," by Aguilar, Day, Manning, and Nutter (Docket No. AUS920030698US1);

"System and Method for Processor Dedicated Code Handling in a Multi-Processor Environment," by Aguilar, Nutter, and Stafford (Docket No. AUS920030791US1);

"System and Method for Managing a Plurality of Processors as Devices," by Aguilar, Day, Nutter, and Stafford (Docket No. AUS920030707US1);

"System and Method for Asymmetric Heterogeneous Multi-Threaded Operating System," by Aguilar, Day, Nutter, and Stafford (Docket No. AUS920030699US1);

"System and Method for Grouping Processors," by Aguilar, Day, Nutter, and Xenidis (Docket No. AUS920030706US1);

"System and Method for Virtual Devices Using a Plurality of Processors," by Aguilar, Day, Nutter, and Xenidis (Docket No. AUS920030705US1);

"System and Method for Encrypting Data Using a Plurality of Processors," by Aguilar, Craft, Day, Hofstee, and Nutter (Docket No. AUS920030704US1);

"System and Method for Manipulating Data with a Plurality of Processors," by Day, Nutter, and To (Docket No. AUS920030718US1);

"System and Method for Solving a Large System of Dense Linear Equations," by Nutter and To (Docket No. AUS920030703US1);

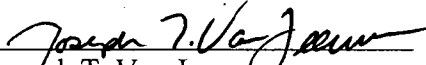
"System and Method for Compiling Source Code for Multi-Processor Environments," by Brokenshire, Minor, Nutter, and To (Docket No. AUS920030701US1);

"System and Method for Balancing Computational Load Across a Plurality of Processors," by Minor, Nutter, and To (Docket No. AUS920030790US1); and

"System and Method for Dynamically Partitioning Processing Across Plurality of Heterogeneous Processors," by Brokenshire, Minor, and Nutter (Docket No. AUS920030702US1).

While these co-pending applications may be "material" pursuant to 37 CFR § 1.56, it has not been ascertained that the aforementioned applications are "related" to the present application.

Respectfully submitted,

By   
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In Place of FORM PTO-1449 (Modified)

Serial No. \_\_\_\_\_

Applicant: Aguilar, et. al.

Filing Date: \_\_\_\_\_

Group: \_\_\_\_\_

Atty. Docket No. AUS920030844US1

**LIST OF PATENTS AND PUBLICATIONS FOR  
APPLICANT'S INFORMATION DISCLOSURE  
STATEMENT**

**U. S. PATENT DOCUMENTS**

Examiner Initial /LV/	Document Number	Date	Name	Class	Subclass	Filing Date if Appropriate
_____	AA 2003/0055984	Jul. 18, 2002	Shimakawa et al.	709	316	Oct. 12, 2001
_____	AB 2002/0156993	Oct. 24, 2002	Suzuoki et al.	712	30	Mar 22, 2001
_____	AC 2002/0138707	Sep 26, 2002	Suzuoki et al.	711	163	Mar 22, 2001
_____	AD 2002/0138701	Sep 26, 2002	Suzuoki et al.	711	154	Mar 22, 2001
_____	AE 2002/0138637	Sep 26, 2002	Suzuoki et al.	709	230	Mar 22, 2001
_____	AF 2002/0135582	Sep 26, 2002	Suzuoki et al.	345	502	Mar 22, 2001
_____	AG 2002/0095523	Jul. 18, 2002	Shimakawa et al.	709	316	Oct. 12, 2001
_____	AH 2002/0060690	May 23, 2002	Tanaka et al.	345	619	Mar. 1, 2001
_____	AI 2002/0046229	Apr. 18, 2002	Yutaka et al.	709	102	Mar. 2, 2001
_____	AJ 6,559,854	May 6, 2003	Oka et al.	345	619	Mar. 1, 2001
_____	AK 2001/0002130	May 31, 2001	Suzuoki	345	420	Dec 15, 2000
_____	AL 6,526,491	Feb. 25, 2003	Suzuoki et al.	711	164	Mar 22, 2001

**FOREIGN PATENT DOCUMENTS**

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation Yes No
_____	AM					
_____	AN					
_____	AO					
_____	AP					

**OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)**

Examiner  
Initial

\_\_\_\_\_ AQ

\_\_\_\_\_ AR

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Examiner: /Lilian Vo/

Date Considered: 04/19/2008

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.